ARYAN

ARYAN SCHOOL OF ENGINEERING & TECHNOLOGY

Dicipline: CSE	Semester: 3rd	Name of the Teaching Faculty: Haraprija Rout	
Subject: Computer System Architecture	No of Days/Week Class Allotted: 4	Semester From date: 15.9.22 To date 21.1.23	No.of Weeks: 15

WEEK	Class Day	Theory Topics
1	1st	Introduction of computer system Architecture
	2nd	Basic structure of computer hardware
	3rd	Computer component
	4th	Functional units.
	5th	
2	1st	Performance Measures
	2nd	Memory addressing & operations.
	3rd	Fundamentals of instructions
	4th	Instruction of sequencing.
	5th	
3	1st	Opercands
	2nd	Op codes
	3rd	Instruction formats
	4th	Addressing modes.
	5th	

WEEK	Class Day	Theory Topics
4	1st	Discussion question of module 1 & 2.
	2nd	Register files
	3rd	Complete instruction execution.
	4th	Steps of instruction.
	5th	
5	1st	Hardware control
	2nd	Micro program control
	3rd	Fetching concept
	4th	Decode
	5th	
6	1st	Execution steps
	2nd	Procedure of processor system.
	3rd	Class test
	4th	instruction of memory system.
	5th	

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WEEK	Class Day	Theory Topics
7	1st	Characteristics of Memory.
	2nd	Memory hierarchy
	3rd	RAM and ROM organization
	4th	Interleaved Memory
	5th	
8	1st	Cache memory
	2nd	virtual Memory.
	3rd	Primary memory or main Memory
	4th	Types of ROM
	5th	
9	1st	Assessment Test
	2nd	Basic concepts of input & output Devices
	3rd	Input - output interface.
	4th	Modes of data transfer
	5th	

WEEK	Class Day	Theory Topics
10	1st	Deffercence between input & output Devices.
	2nd	Programmed input output transfer
	3rd	Intercrupt driven in input & output devices.
	4th	DMA (Direct memorcy Access)
	5th	
11	1st	Input - output processor.
	2nd	Basic and brief discussion of input-output system.
	3rd	Practice test with coone question.
	4th	Input - output interface & Bus architecture.
	5th	
12	1st	Bus & system Bus, Types of system bus.
	2nd	Briefly discussion in types of system bus.
	3rd	Data bus, Address bus, Control bus.
	4th	Structurce of bus.
	5th	

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WEEK	Class Day	Theory Topics
13	1st	Basic parameters of Bus design.
	2nd	SCSI
	3rd	USB
	4th	Test
	5th	
14	1st	Discussion in question & answer in Bus architecture.
	2nd	Introduction in parallel processing.
	3rd	Key points of processing.
	4th	Parallel Processing.
	5th	
15	1st	Linear pipeline.
	2nd	Multiprocessor
	3rd	Flynn's classification
	4th	overall discussion of computer system architecture.
	5th	